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Application / Conf. No. 09/684,529 / 1502

Filing Date October 06, 2000

First Named Inventor John D. Logue

Examiner Name Eva Y. Zheng

Art Unit 2634

Patent No.

Attorney Docket Number X-735 US

ENCLOSURES (check all that apply)

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Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

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Date	September 29, 2005	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040	

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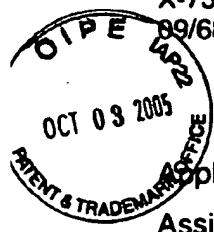
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X-735 US
09/684,529

PATENT
Conf. No. 1502

IN THE UNITED STATES PATENT OFFICE

Applicants: John D. Logue, et al.
Assignee: Xilinx, Inc.
Title: Synchronized Multi-Output Digital Clock Manager
Serial No: 09/684,529 Filed: October 6, 2000
Examiner: Eva Y. Zheng Art Unit: 2634
Docket No: X-735 US Conf. No.: 1502

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AMENDMENT IN RESPONSE TO THE FINAL OFFICE ACTION AFTER RCE

Dear Sir:

Applicants submit the following amendment in response to the Final Office Action after the RCE mailed on August 8, 2005.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.